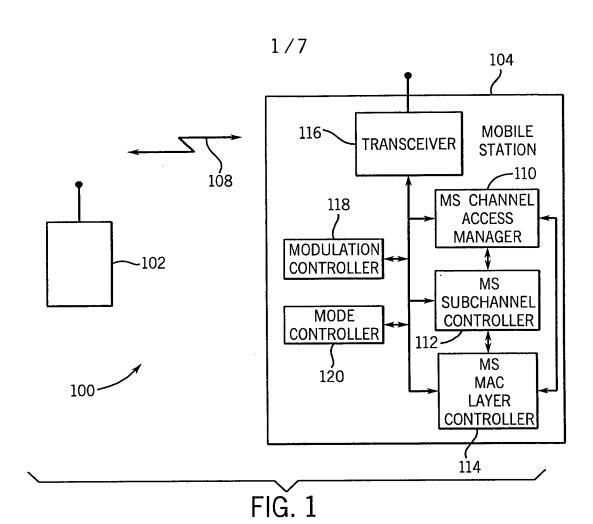
## **Amendments**

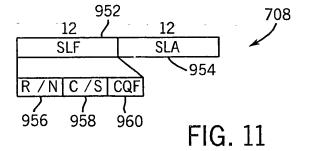
Please amend the above-identified application as follows:

## In the Drawings:

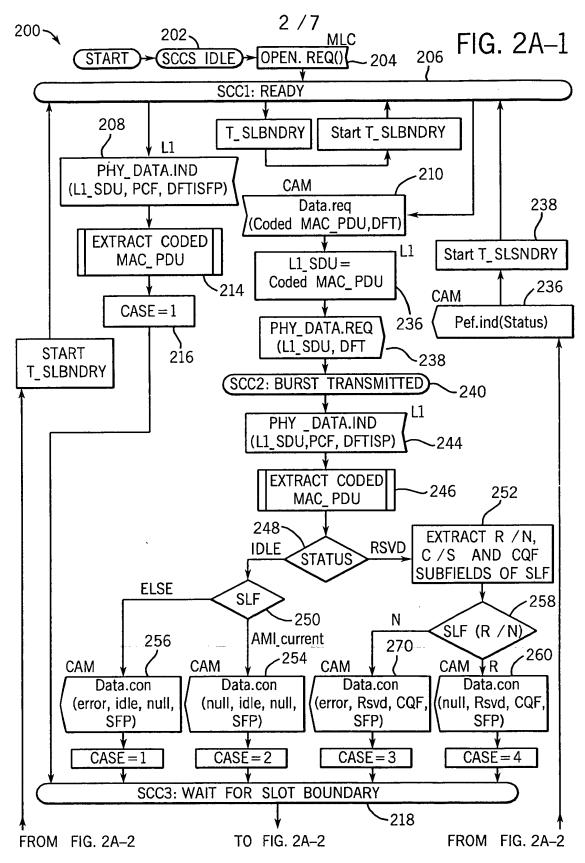
Kindly substitute the enclosed FIGS. 1-11 for the prior FIGS. 1-11.













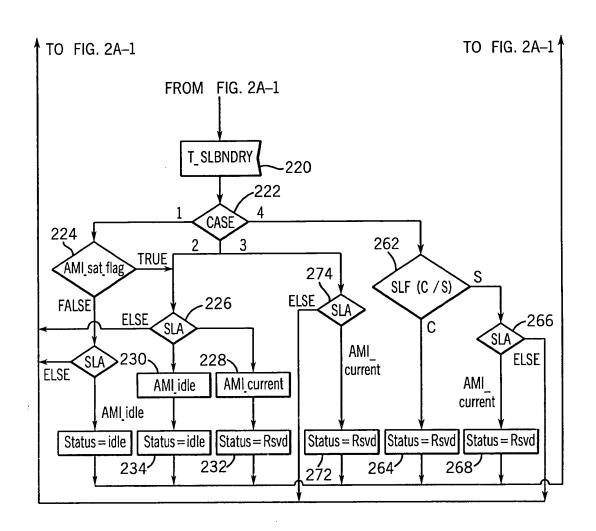
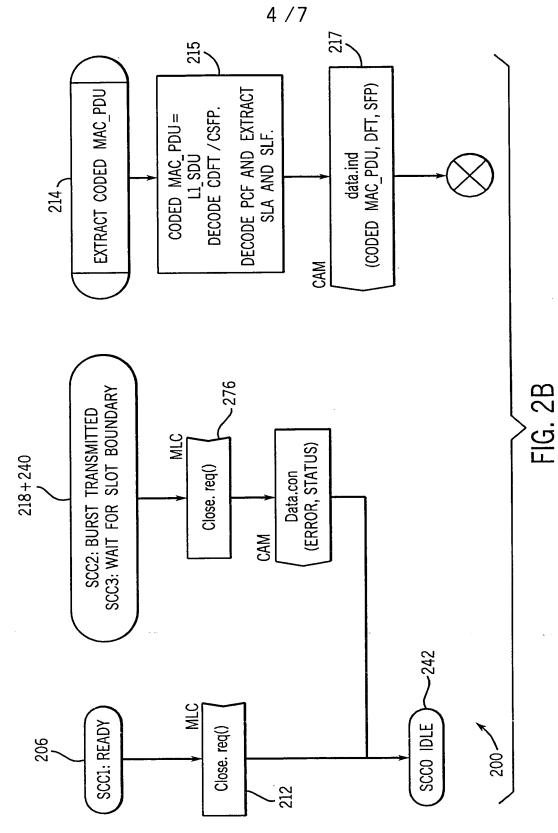
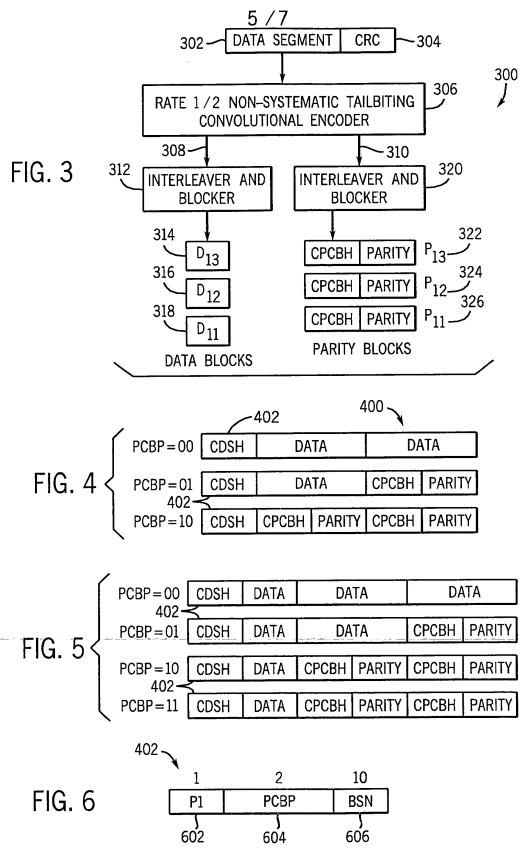


FIG. 2A-2











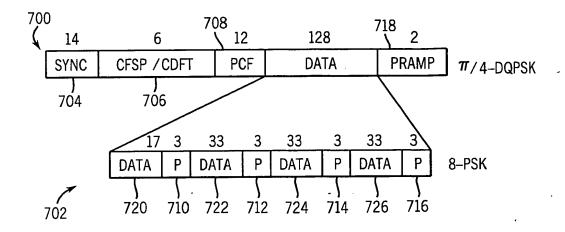


FIG. 7

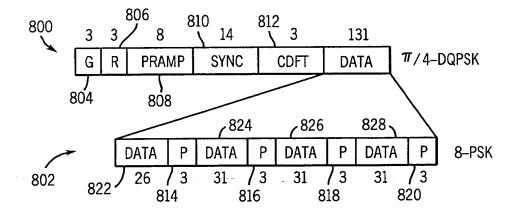


FIG. 8



FIG.	PEAK TRIPLE RATE THROUGHPUT (KB / S)	PDU SIZE (BITS)	436 CDSH SIZE (BITS)	NUMBER OF RLP BLOCKS PER PDU	MODULATION	RLP BLOCK SIZE (BITS)	CRC SIZE (BITS)	RLP SEGMENT SIZE (OCTETS)
FIG	32.4 43.2		31 36	291 436	245	8 4		8-PSK 16-LEVEL
	21.6	$\dashv$	∞	218	173	2		π/4-DQPSK
	52.8		7	524	381	4		16- LEVEL
	39.6		7.	357	293	3		8-PSK
i.	26.4		.2	262	205	2	_	#/4-DQPSK
\	52.8		2	512	381	4		16-LEVEL
§,	39.6		œ	348	293	က	_	8-PSK
Č	26.4		9	256	205	2	_	#/4-DQPSK
	CONVOLUTIONAL THROUGHPUT (RB / S)	L (S)	UTIONAL UTPUT (BI'	CONVOLUTIONAL ENCODER OUTPUT (BITS)	(BITS)	OCTET RLP SEGMENTS	SS	
	K TRIPLE RATE	<u> PEE</u>	PUNCTURE	<b>RATE 1/2</b>	DIRECTION   MODULATION   NUMBER OF 11   PDU SIZE	BER OF 11	MONIN	MODULATION

7 / 7

						`	*				i	
PEAK TRIPLE	RATE	THROUGHPUT	(KB /S)	29.6	44.4	59.2	29.7	45.6	59.4	24	36	48
<u></u>	SIZE	(BITS)		256	348	512	762	357	524	218	327	436
CDSH	SIZE	(BITS)		48	36	96	20	39	100	48	36	96
NUMBER	OF RLP	BLOCKS	PER PDU	2	က	4	2	m	4	2	က	4
MODULATION				#/4-DQPSK	8-PSK	16-LEVEL	#/4-DQPSK	8-PSK	16- LEVEL	#/4-DQPSK	8-PSK	16-LEVEL
			(BITS)	104			106			85		
CRC	SIZE	(BITS)		16			14			15		
RLP	SEGMENT	SIZE	(OCTETS)	37			38			30	-	
DIRECTION				DOWNLINK			UPLINK	; ; ;		UPLINK	(ABBREVIATED)	